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Modeling and control in this article concentrates on a current-mode buck converter, but the methods described here can be applied to voltage-mode buck converters as well. Appendix I. The

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power stage transfer functions in Figure 2 are as follows. where: where: L_1 is the primary inductance. C_1 is the primary capacitance.

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model. The EPFL EKV version 2.6 MOST model is a charge-based compact

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model. It consistently describes effects on charges, trans-capacitances, drain Thermoelectrical Interactions," Modeling in Analog design, Vol. 2, in series on Current Issues in Electronic Modeling, , Ch. 5, pps 121-143, Eds: Jean-Michel BERGE.

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ADP2386 Modeling Simulation and Test Result The ADP2386 is a synchronous PCM CCM buck regulator from Analog Devices. It ranges from 20 V input voltage down to 0.6 V output voltage at up to 6 A output current, with switching frequency ranges from 200 kHz to 1.2 MHz.

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MOSFET DEVICE MODELING FOR ANALOG CIRCUITS DESIGN

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CMOS Circuit Design

You can use Behavioral Modeling in the domain of analog simulation to model new device types and for black-box modeling of complex systems. In this document, examples are used to show how the Analog Behavioral Modeling feature of PSpice can be used to:

- Calculate square roots
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Analog Behavioral Modeling | PSpice

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modeling of a current mode control boost converter operating in continuous conduction mode. It details how to properly design both the control loop and the compensation loop to maintain

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the stability of the current-mode regulator. The simulation results and experimental results are given and contrasted based on a 3.5V-5.5V DC input, 12V DC

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His current research interests include design of analog and mixed-signal integrated circuits, modeling of deep sub micrometer MOSFETs, MESFETs, HFETs, and novel device structures for application in circuit simulators. Prof. Ytterdal is a member of the Norwegian Academy of Technological Sciences.

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9780471498698: Device Modeling for Analog and RF CMOS ...

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